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SAMSUNG ELECTRONICS

승 인 서

SEC CODE	
품명 (Description)	LCD-Panel
규격 (Specification)	32inch, NNB, HD, 60Hz, LED
Maker P/N	LSJ320AP01
적용기종 (적용모델)	
특기사항	

기안 (DRAWING)	심사 (CHECK)	합의 (AGREEMENT)	승인 (APPROVAL)
2010. .	2010. .	2010. .	2010. .
승인기간	승인일로부터		

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PI

Customer : **DATE :19. Jan. 2011**

SAMSUNG TFT-LCD

MODEL : **LSJ320AP01**

Any Modification of Specification is not allowed without SEC's Permission.

NOTE :

Customer's *Approval*	
SIGNATURE	DATE

APPROVAED BY	DATE
Heo Jeongmin	19. Jan. 2011
PREPARED BY Bong U Lee	DATE 19. Jan. 2011

LCD Business

Samsung Electronics Co . , LTD.

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Revision History

Date	Rev. No	Page	Summary
19. Nov. 2010	000	all	First issued
19. Jan 2011	001	25 page	Storage

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General Description

Description

LSJ320AP01 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and source PBAs. The resolution of a 32" is 1366 X 768 and this model can display up to 16.7 Million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

Features

- High contrast & aperture ratio with wide color gamut
- SVA (Super Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- HD resolution (16:9)
- LVDS (Low Voltage Differential Signaling) interface (1pixel/clock)

General Information

Items	Specification	Unit	Note
Panel Size	713.0(H_{TYP}) x 412.80 (V_{TYP})	mm	±0.2mm
	1.76(D_{MAX})		±0.15mm
Weight	1.1(Max.)	kg	±0.1Kg
Pixel Pitch	0.51075 (H) ×0.17025 (V)	mm	
Active Display Area	697.6845 (H) ×392.256 (V)	mm	
Surface Treatment	5.5 Haze, Hard-Coating (2H)		
Display Colors	8 Bits-True, 16.7M	colors	
Number of Pixels	1366 X 768	pixel	
Pixel Arrangement	RGB horizontal stripe		
Display Mode	Normally Black		

1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

(V_{SS} = 0 V)

Item		Symbol	Min.	Max.	Unit	Note
Power Supply Voltage		V_{DD}	10.8	*13.2*	V	(1)
Storage temperature		T_{STG}	-20	65	℃	(2)
Glass surface temperature (Operation)	Center	T_{OPR}	0	50	℃	(2)

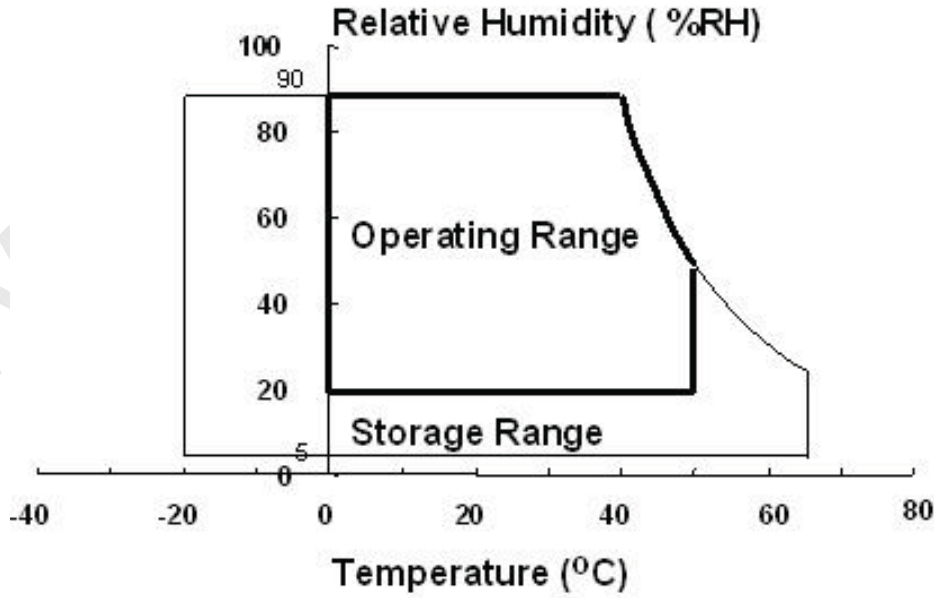
Note (1) Ta= 25 ± 2 °C

(2) Temperature and relative humidity range are shown in the figure below.

a. 90 % RH Max. (Ta ≤ 39 °C)

b. Relative Humidity is 90% or less. (Ta > 39 °C)

c. No condensation



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2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.
Measuring equipment : TOPCON RD-80S, TOPCON SR-3 ,ELDIM EZ-Contrast

(Ta = 25 ± 2°C, VDD=12.0V, fv= 60Hz, f_{DCLK}=78MHz)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R	Normal θL,R=0 θU,D=0 Viewing Angle	3000	4000	-		(1) SR-3
Response Time	G-to-G	Tg		-	8	16	msec	(3) RD-80S
Luminance of White (Center of screen)		Y_L		250	300	-	cd/m²	(4) SR-3
Color Chromaticity (CIE 1931)	Red	Rx		TYP. -0.03	0.640	TYP. +0.03		(5),(6) SR-3
		Ry			0.330			
	Green	Gx			0.300			
		Gy			0.640			
	Blue	Bx			0.150			
		By			0.060			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut		-		69	72	-	%	(5) SR-3
Color Temperature		-		7,000	10,000	13,000	K	
Viewing Angle	Hor.	θ_L	C/R≥10	75	89	-	Degree	(6) EZ-Contrast
		θ_R		75	89	-		
	Ver.	θ_U		75	89	-		
		θ_D		75	89	-		
Brightness Uniformity (9 Points)		B_{uni}		-	-	25	%	(2) SR-3

- Optical characteristics are based on VD standard module
- The measurement shall be executed 60 minutes after lighting at rating.
- Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at 25 ± 2°C for stabilization of the back light. This should be measured in the center of screen.
Environment condition : Ta = 25 ± 2 °C

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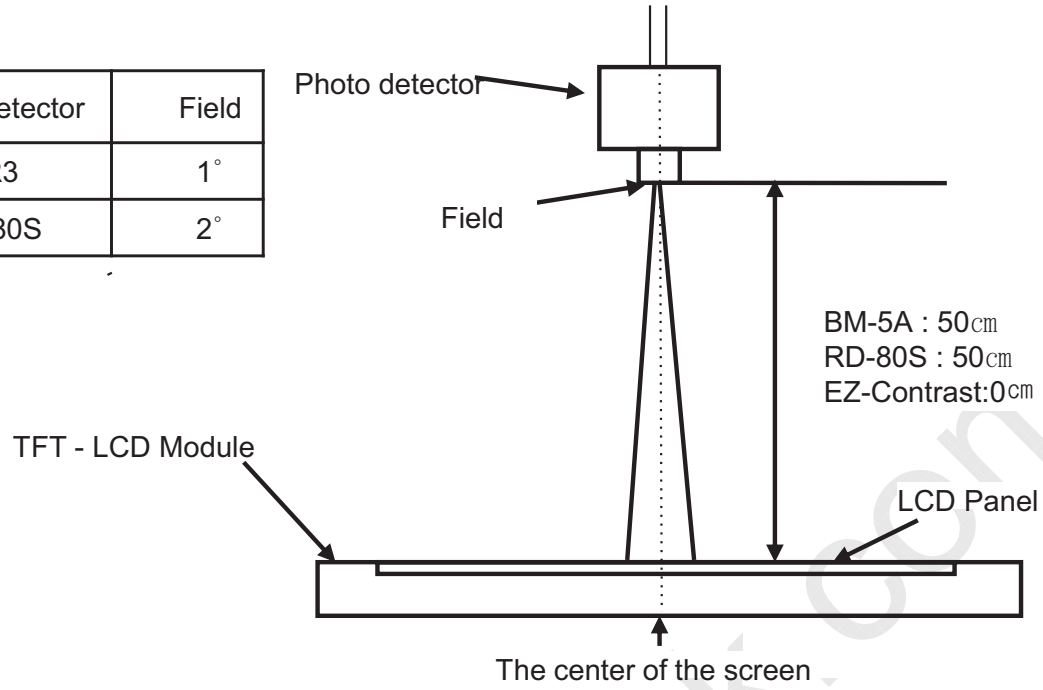
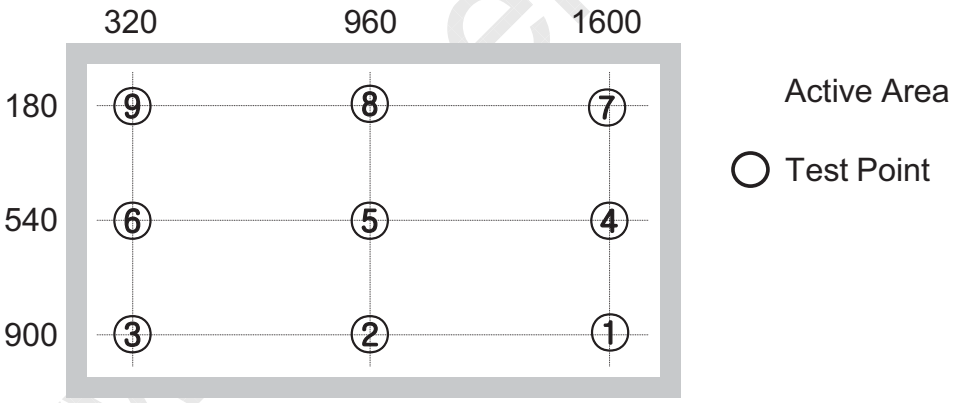
Photo detector	Field
SR3	1°
RD-80S	2°



- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)
: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G\max}{G\min}$$

Gmax : Luminance with all pixels white
Gmin : Luminance with all pixels black

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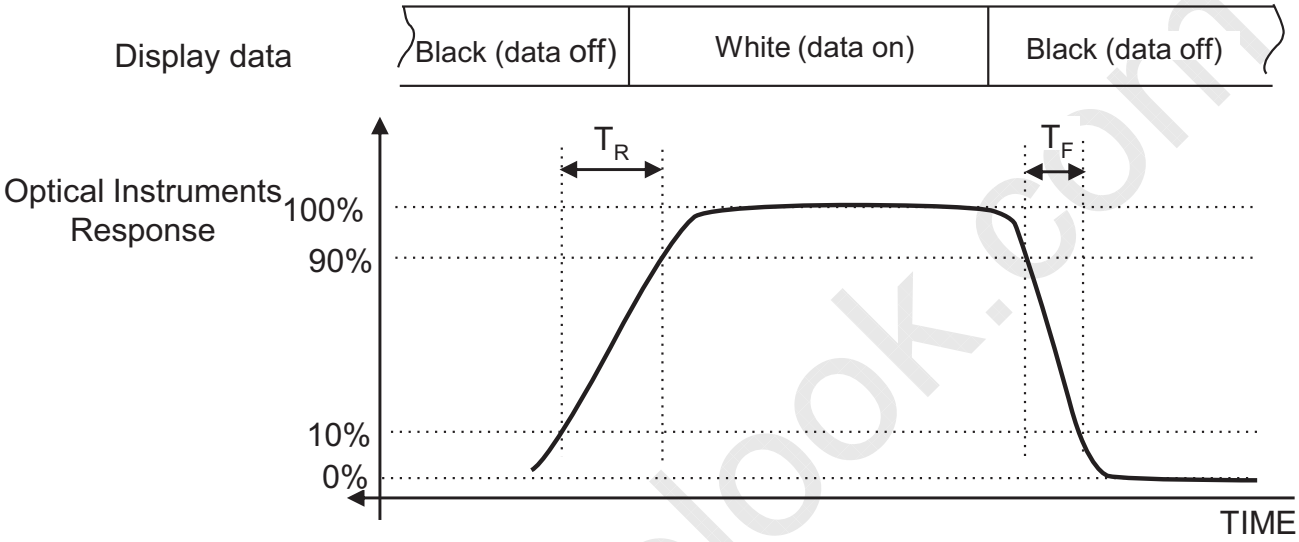
Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$Buni = 100 * \frac{(B\max - B\min)}{B\max}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

Note (3) Definition of Response time : Sum of Tr, Tf

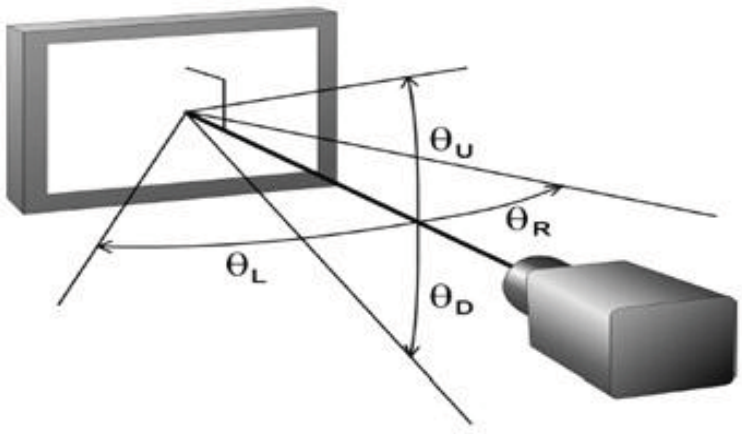


※ G-to-G : Average response time between Gray to Gray (Scale)

Note (4) Definition of Luminance of White : Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle
: Viewing angle range (C/R ≥10)



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3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25°C ± 2 °C

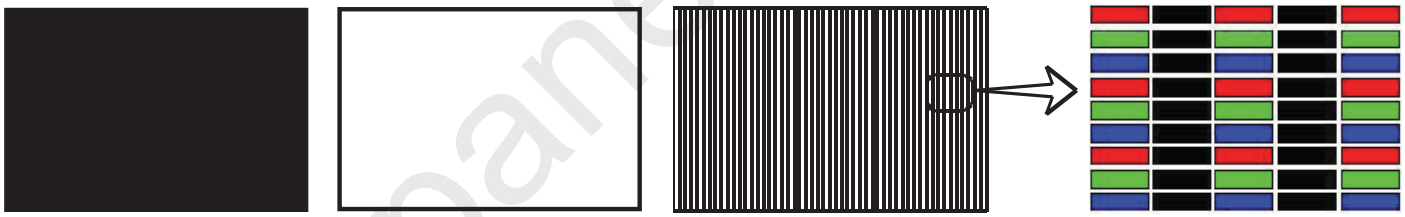
Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	I_{DD}	250	450	650	mA	(2),(3)
	(b) White		250	450	650	mA	
	(c) Sub V-stripe		500	700	900	mA	
Vsync Frequency		f_V	48	60	66	Hz	
Hsync Frequency		f_H	44	48	53	kHz	
Main Frequency		Fdclk	72	78	85	MHz	
Rush Current		I_{RUSH}	-	-	4	A	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD}.

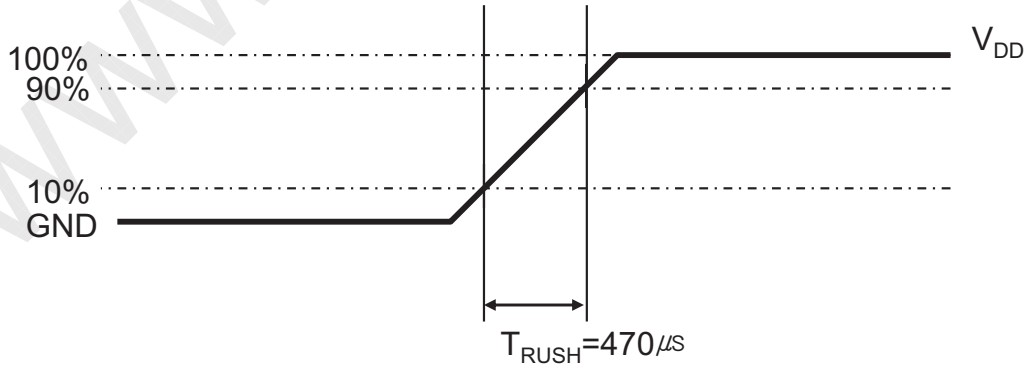
(2) f_V= 60 Hz, f_{DCLK} =78MHz, V_{DD} = 12.0V, DC Current.

(3) Power dissipation check pattern (LCD Module only)

a) Black Pattern b) White Pattern c) Sub V-stripe Pattern



(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when T_{RUSH}. is 470μs.

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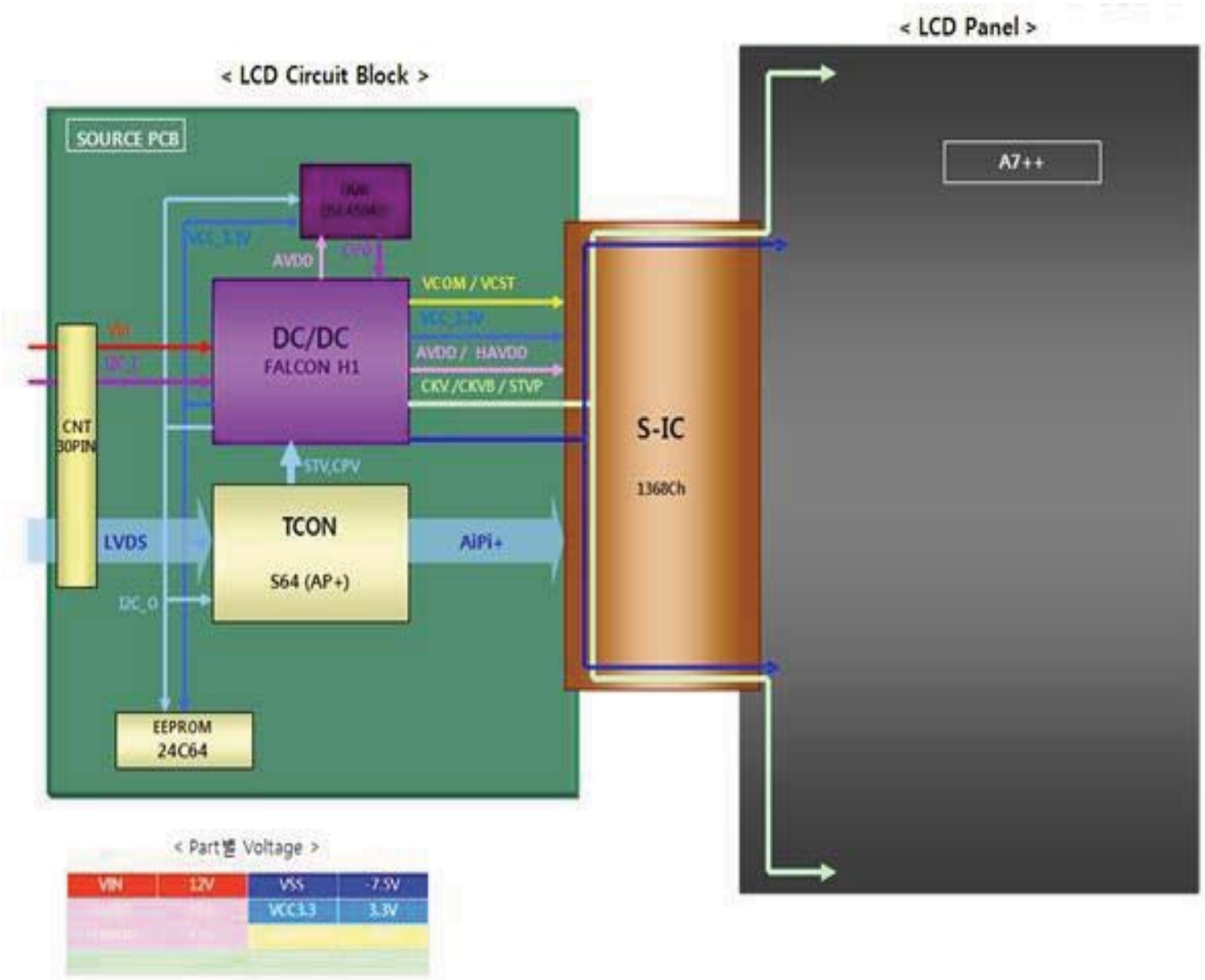
4. Block Diagram



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5. Input Circuit Pin Assignment

5.1. Input Signal

Connector : SMD-A(YOUNHO)

No	Signal	REMARK
1	WPN	WPN
2	SCL	I2C interface
3	SDA	
4	GND	GND
5	LV0_N	LVDS
6	LV0_P	
7	GND	GND
8	LV1_N	negative LVDS differential data input (0)
9	LV1_P	positive LVDS differential data input (0)
10	GND	GND
11	LV2_N	negative LVDS differential data input (1)
12	LV2_P	positive LVDS differential data input (1)
13	GND	GND
14	LVCLK_N	negative LVDS differential data input (Clock)
15	LVCLK_P	positive LVDS differential data input (Clock)
16	GND	GND
17	LV3_N	negative LVDS differential data input (3)
18	LV3_P	positive LVDS differential data input (3)
19	GND	GND
20	N.C	
21	LVDS_SEL	LVDS OPTION (Low: JEIDA, High: VESA)
22	WPN	WPN
23	GND	GND
24	GND	
25	N.C	
26	VIN	Power Supply : +12V
27	VIN	
28	VIN	
29	VIN	
30	VIN	

Note1) No Connection: This PINS are only used for SAMSUNG internal using.
Note2) LVDS OPTION : If this PIN is HIGH (3.3 V) → Normal LVDS format
LOW (GND) → JEIDA LVDS format
SEQUENCE : On = V_{DD}(T1) → LVDS Option → Interface Signal(T2)
OFF = Interface Signal(T3) → LVDS Option → V_{DD}

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Note(1) Pin number starts from Left side

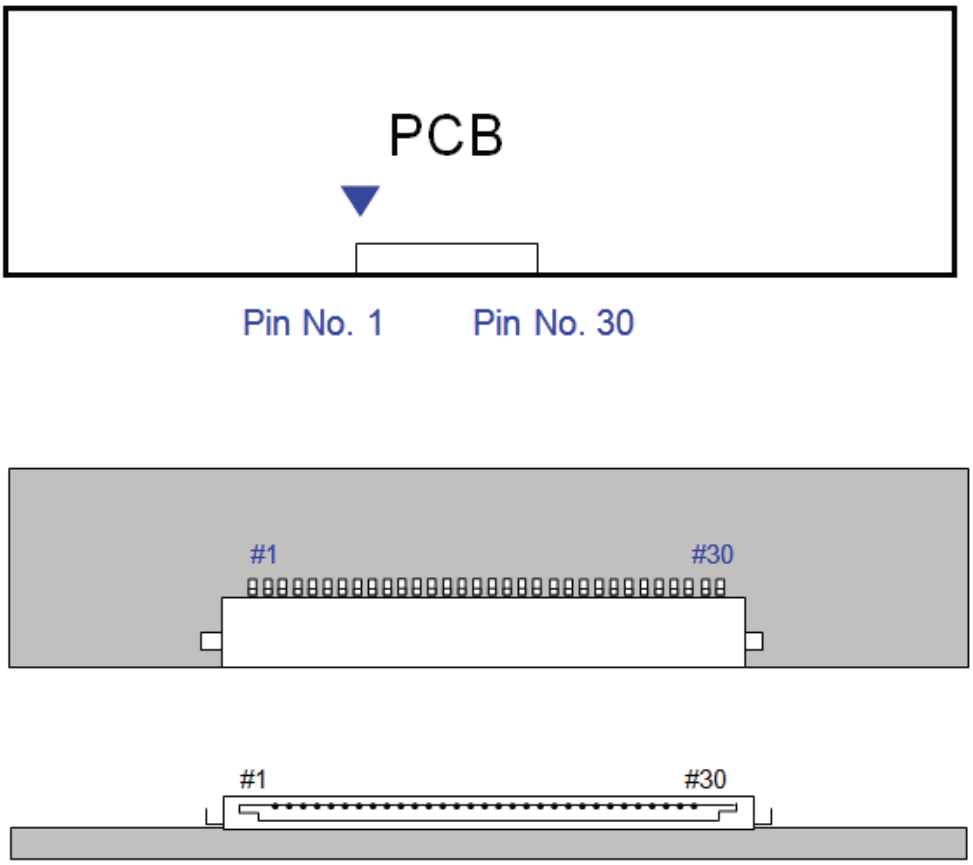

Fig. Connector diagram

a. Power GND pins should be connected to the LCD's metal chassis.
b. All power input pins should be connected together.
c. All NC pin should be separated from other signal or power.

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5.2 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA & Normal)

	LVDS pin	JEIDA -DATA	VESA -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R2	R0
	TxIN/RxOUT1	R3	R1
	TxIN/RxOUT2	R4	R2
	TxIN/RxOUT3	R5	R3
	TxIN/RxOUT4	R6	R4
	TxIN/RxOUT6	R7	R5
	TxIN/RxOUT7	G2	G0
TxOUT/RxIN1	TxIN/RxOUT8	G3	G1
	TxIN/RxOUT9	G4	G2
	TxIN/RxOUT12	G5	G3
	TxIN/RxOUT13	G6	G4
	TxIN/RxOUT14	G7	G5
	TxIN/RxOUT15	B2	B0
	TxIN/RxOUT18	B3	B1
TxOUT/RxIN2	TxIN/RxOUT19	B4	B2
	TxIN/RxOUT20	B5	B3
	TxIN/RxOUT21	B6	B4
	TxIN/RxOUT22	B7	B5
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R0	R6
	TxIN/RxOUT5	R1	R7
	TxIN/RxOUT10	G0	G6
	TxIN/RxOUT11	G1	G7
	TxIN/RxOUT16	B0	B6
	TxIN/RxOUT17	B1	B7
	TxIN/RxOUT23	RESERVED	RESERVED

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6.2 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY	DATA SIGNAL																								GRAY SCALE LEVEL
		RED								GREEN								BLUE								
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7	
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R252
	↓ LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G252
	↓ LIGHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B252
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note) Definition of Gray :
Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)
Input Signal : 0 = Low level voltage, 1 = High level voltage

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7. Interface Timing

7.1 Timing Parameters

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	72	78	85	MHz	-
Hsync		F_H	44	48	53	KHz	-
Vsync		F_V	48	60	66	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	768	-	Lines	-
	Vertical Total	T_V	776	802	1200	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1366	-	Clocks	-
	Horizontal Total	T_H	1480	1624	2000	clocks	-

Note)

(1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

(2) Internal V_{DD} = 3.3V

(3) Spread spectrum

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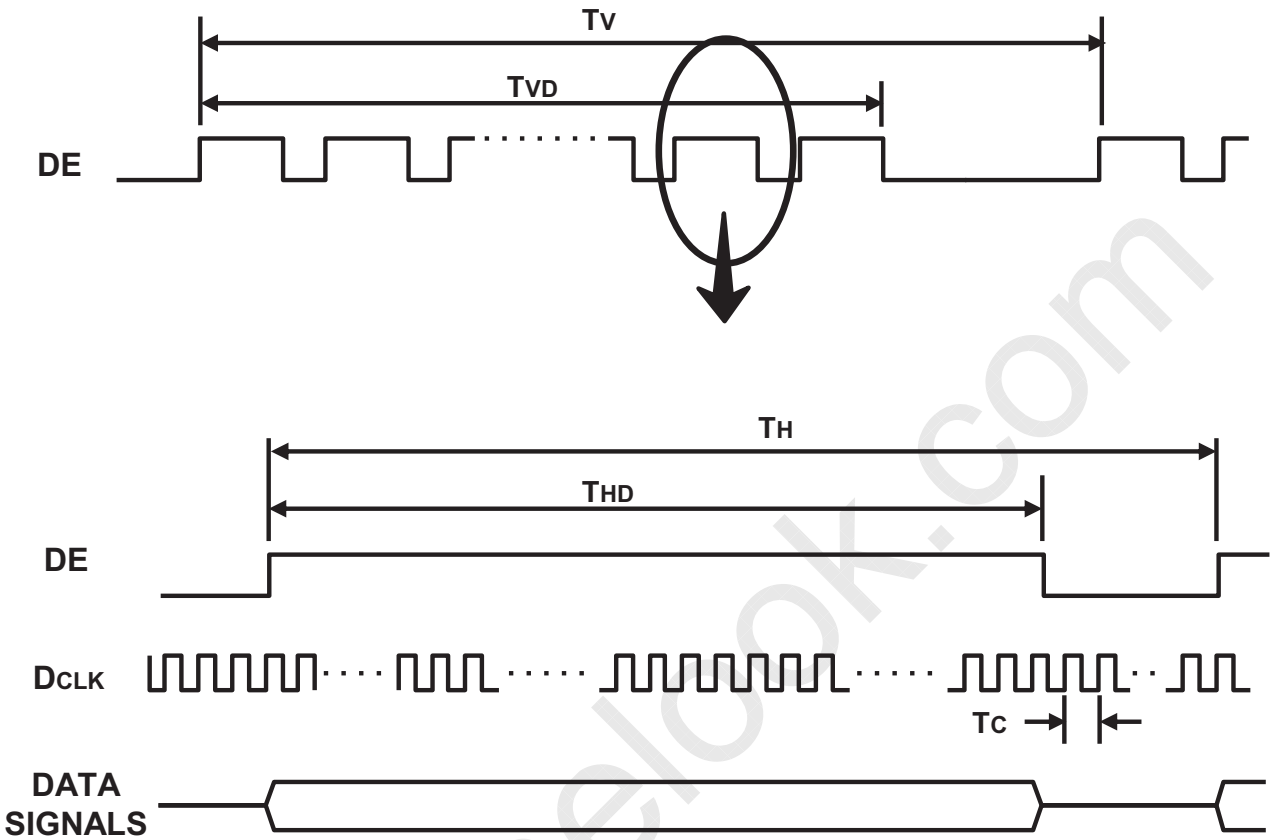
7.2 Timing diagrams of interface signal



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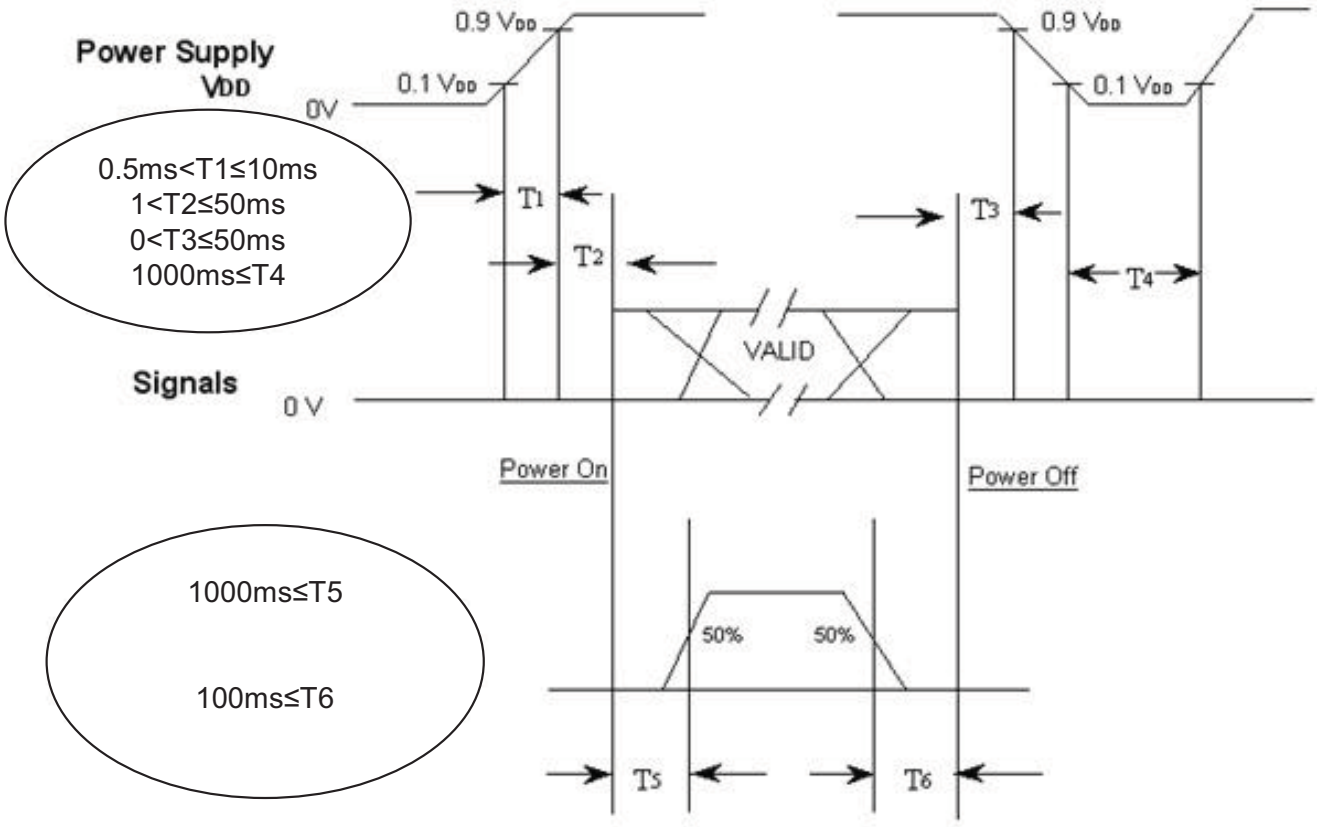
7.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1 : V_{DD} rising time from 10% to 90%
T2 : The time from V_{DD} to valid data at power ON.
T3 : The time from valid data off to V_{DD} off at power Off.
T4 : V_{DD} off time for Windows restart
T5 : The time from valid data to B/L enable at power ON.
T6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

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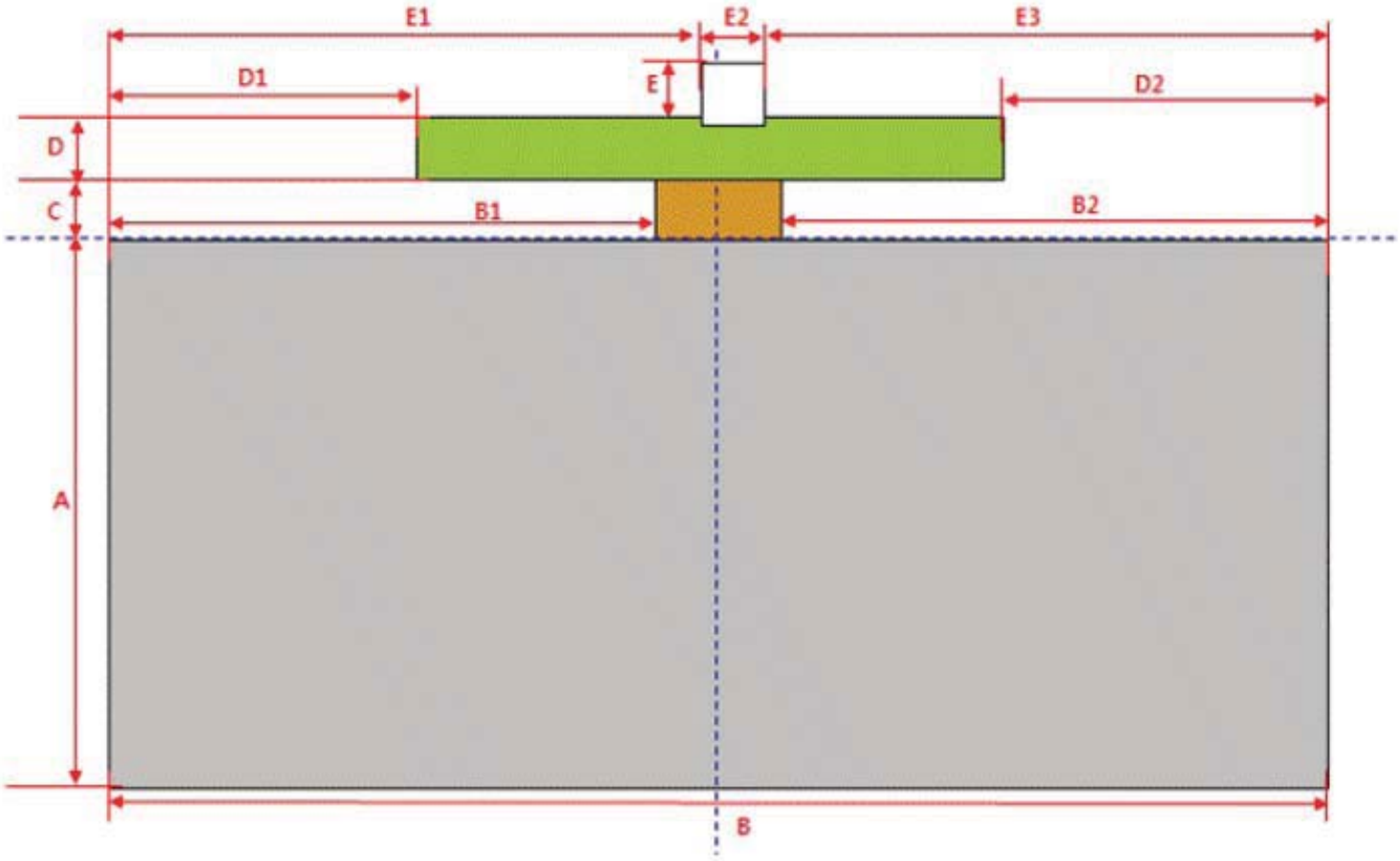
8. Outline Dimension



A	B	C	D	D1	D2	E	E1	E2	E3
412.8	713	20.8	25	206.52	206.48	40	350.52	32	330.48

9. Reliability Test

Item	Test condition	Quantity
Temperature Step stress	-20℃ ~ 65℃, 440Cycle	4EA
HTOL	50℃operation,500hr 60℃operation,500hr	8EA
LTOL	-5℃operation, 500hr	4EA
RTOL	25℃,	4EA
HTS	70℃storage, 500hr	4EA
LTS	-25℃storage, 500hr	4EA
THB	50℃ / 90%RH, 30sec On / Off , 500hr operation	4EA
WHTS	60℃ / 75%RH, 500hr	4EA
ESD (Driver-IC)	Input:±4kV,150pF/330Ω,Input Con.Pin,3 Times/Pin	3EA
T/S	-20 ℃ ~ 60 ℃, 100Cycle	4EA

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these should be no change which may affect practical display functions.

* HTOL/ LTOL : High/Low Temperature Operating Life
** THB : Temperature Humidity Bias
*** HTS/LTS : High/Low Temperature Storage
**** WHTS : Wet High Temperature Storage

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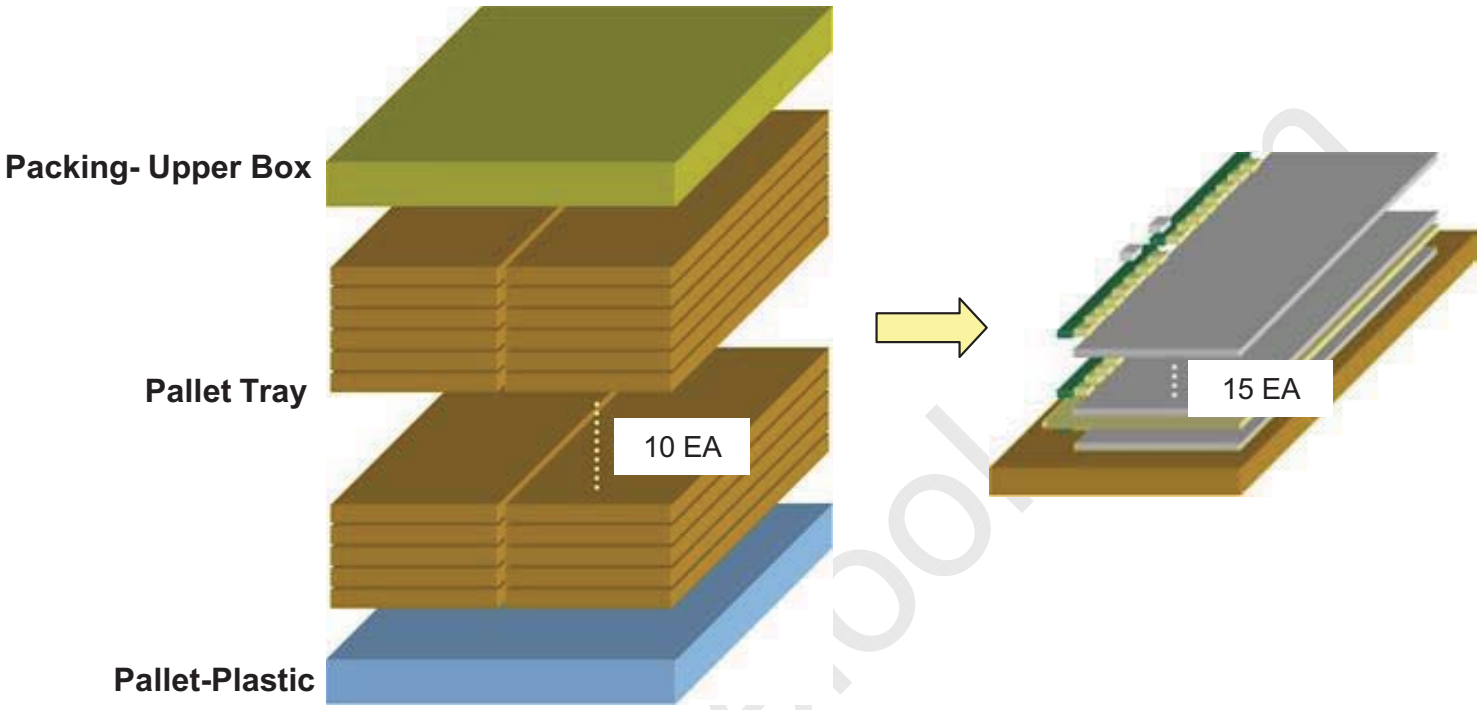
10. PACKING

10.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and PCABS tray as shock absorber

(2) Packing Method



10.2 Packing Specification

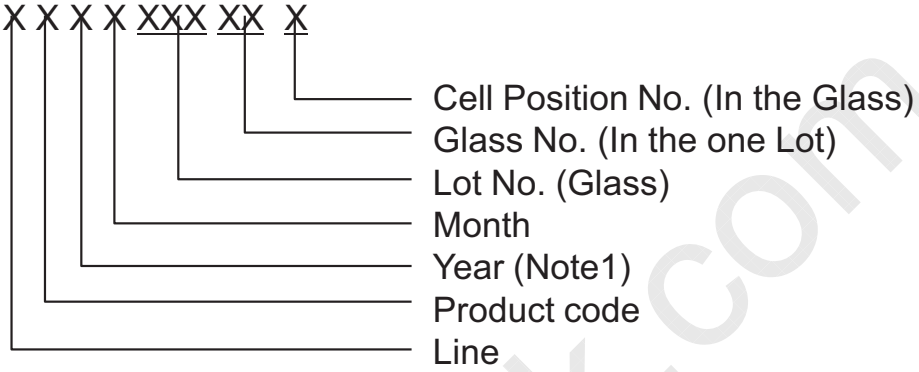
Tray spec

ITEM	Specification	Remark
LCD Packing	5ea / Box	1. 2.00 Kg / LCD (10[ea]/Tray, 10[ea]/Pallet) 2. 0.04 Kg / Middle sheet (16[ea]/Tray, 160[ea]/Pallet) 2. 5.52 Kg / Panel tray (30ea/Pallet) 3. 2.00 Kg / Packing-Upper Box (1ea)
Pallet-Plastic	1Box / Pallet (W1200,L850,H120 , YELLOW)	1. Pallet weight = 23 kg 2. 23 Kg / Pallet
Packing Direction	Vertical	
Pallet size	H x V x height	1475mm(H) x 1150mm(V) x 1131mm(height)
Pallet weight	294 kg	Pallet(23kg) + Panel tray(165.6kg) + Panel(280kg) + Middle sheet(7.9Kg) + Packing-Upper Box (2.0kg)

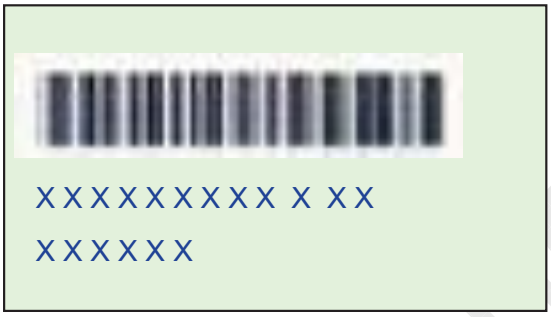
11. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Part number : LSJ320AP01
(2) Revision: One letter
(3) Control: One letter
(4) Lot number : X X X X XXX XX X

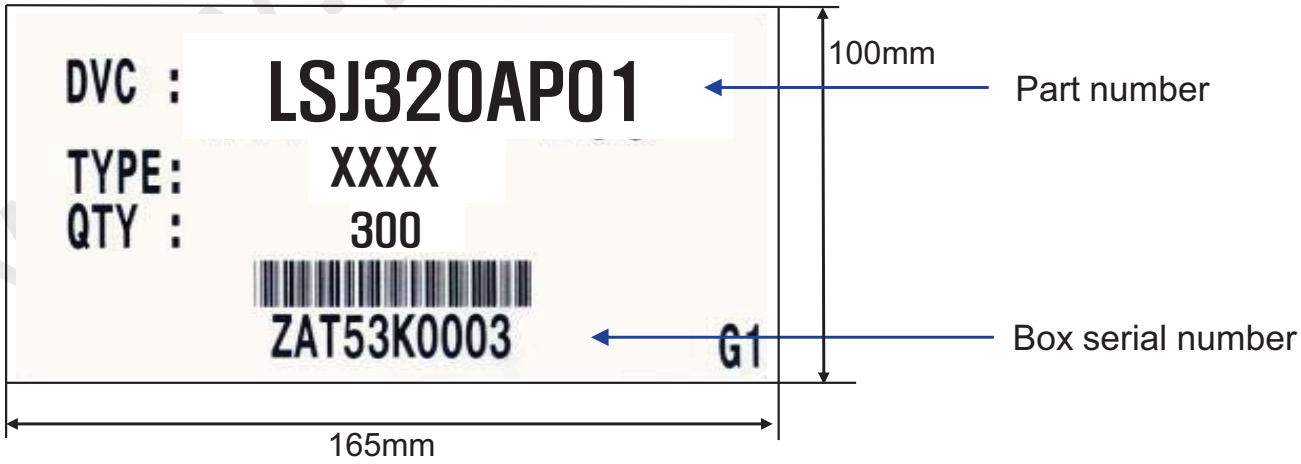


(4) Control PBA



-Color: Green

(5) Packing box attach



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12. General Precautions

12.1 Handling

(a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.

(b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.

(c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFL back light.

(d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.

(e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.

(f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.

(g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.

(i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or Semiconductor would be damaged.

(j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(k) Do not disassemble the Module.

(l) Do not disassemble shield case of inverter & LVDS board.

(m) Do not connect N.C pins. (Samsung internal use only)

(n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handle a module

(o) Pins of I/F connector should not be touched directly with bare hands.

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* Process Control Standard

	Item	Management standard value and performance standard
1	Anti-static mat (shelf)	1 to 50 [Mohm]
2	Anti-static mat (floor, desk)	1 to 100 [Mohm]
3	Ionizer	Attenuate from ± 1,000V to ± 100V within 2 sec
4	Anti-static wrist band	0.8 to 10 [Mohm]
5	Anti-static wrist band entry and ground resistance	Below 1,000 [ohm]
6	Temperature	0 ~ 35℃
7	Humidity	60 to 70 [%RH]

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12.2 Storage

We highly recommend to comply with the criteria in the table below

ITEM	Unit	Min.	Max.
Storage Temperature	(℃)	5	40
Storage Humidity	(%rH)	35	75
Storage life	12 months		
Storage Condition	- The storage room should provide good ventilation and temperature control. - Products should not be placed on the floor, but on the Pallet away from a wall. - Prevent products from direct sunlight, moisture nor water; Be cautious of a build up of condensation. - Avoid other hazardous environment while storing goods. - If products delivered or kept in conditions of over the storage period of 3 months, the recommended temperature or humidity range, we recommend you leave them at a temperature of 20℃ and a humidity of 50% for 24 hours.		

12.3 Operation

(a) Do not connect or disconnect the Module in the "Power On" condition.

(b) Power supply should always be turned on/off by the "Power on/off sequence"

(c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

(d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

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12.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
- Temperature : 20±15℃
- Humidity : 55±20%
- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

12.5 Others

(a) Ultra-violet ray filter is necessary for outdoor operation.

(b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.

(c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.

(d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.
To avoid image sticking, it is recommended to use a screen saver.

(e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.

(f) Please contact SEC in advance when you display the same pattern for a long time.

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